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Dated: March 10, 2006

Signature: 

(Andrew T. Zidel)

SCEI 3.0-058
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Suzuoki et al.

Application No.: 09/816,752

Group Art Unit: 2154

Filed: March 22, 2001

Examiner: J. Chang

For: PROCESSING MODULES FOR COMPUTER
ARCHITECTURE FOR BROADBAND NETWORKS

Commissioner for Patents
P.O. Box 1450
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INFORMATION DISCLOSURE STATEMENT

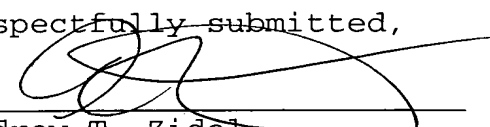
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It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

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Dated: March 10, 2006

Respectfully submitted,

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				Application Number	09/816,752-Conf. #6544
				Filing Date	March 22, 2001
				First Named Inventor	Masakazu Suzuoki
				Art Unit	2154
				Examiner Name	J. Chang
Sheet	1	of	1	Attorney Docket Number	SCEI 3.0-058

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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